

JC5/1579

	Class	Subclass

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PATENT NUMBER

U.S. UTILITY Patent Application

 SCANNED <u>SP33</u> Q.A. <u>10</u>	O.I.P.E.	PATENT DATE
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APPLICATION NO. 09/915792	CONT/PRIOR	CLASS 744 716	SUBCLASS 716	ART UNIT 2483 2825	EXAMINER Dingay
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Method and apparatus for determining critical timing path sensitivities of macros in a semiconductor device

TIME

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="text-align: center;">(Assistant Examiner) _____ (Date) _____</div>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
	<div style="text-align: center;">(Primary Examiner) _____ (Date) _____</div>			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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